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Dated: July 9, 2003

Signature: 

Docket No.: 29926/39502
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Won-Ho Lee .

Application No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filed: July 9, 2003 (concurrently herewith)

Examiner: Not Yet Assigned

For: METHOD FOR ISOLATING HYBRID
DEVICE IN IMAGE SENSOR

INFORMATION DISCLOSURE STATEMENT (IDS)

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

A copy of each reference on PTO/SB/08 is attached.

A concise explanation of relevance of the items listed on form PTO/SB/08 is:

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- [] in the form of an English language copy of a Search Report from a foreign patent office, issued in a counterpart application, which refers to the relevant portions of the references

While the information and references disclosed in this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

Applicant submits that the particular month of publication for the references disclosed in this Information Disclosure Statement is not in issue since the year of publication for the references is sufficiently earlier than the effective U.S. filing date and any foreign priority date. Therefore, it is submitted that this Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 13-2855, under Order No. 29926/39502. A duplicate copy of this paper is enclosed.

Dated: July 9, 2003

Respectfully submitted,

By Marla L. Hudson

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Registration No.: 43,680

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Attorney for Applicant

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	Not Yet Assigned
				Filing Date	July 9, 2003
				First Named Inventor	Won-Ho Lee
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	29926/39502

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Fujieda et al., "Dependence of Si PN Junction Perimeter Leakage on the Channel-Stop Boron Dose and Interlayer Material" (IEEE ELECTRON DEVICE LETTERS, VOL. 20, NO. 8, AUGUST 1999, Pages 418-420)	
		Morris et al., "An Accurate and Efficient Model for Boron Implants Through Thin Oxide Layers into Single-Crystal Silicon" (IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 8, NO. 4, NOVEMBER 1995, Pages 408-413)	
		Walter et al., "Dopant Channeling as a Function of Implant Angle for Low Energy Applications" (IEEE, 1999, Pages 126-129)	
		Son et al., "Blanket Tilt Implanted Shallow Trench Isolation (BTI-STI) Process for Enhanced DRAM Retention Time Characteristics" (IEEE, 1999, Pages 122-124)	

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